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## » Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

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IEEE STD IEEE Standard

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- ☐ 1. **Recovery and Drift Dynamics of Resistance and Threshold Voltages in Pt Memories**  
Ielmini, D.; Lacaita, A. L.; Mantegazza, D.;  
[Electron Devices, IEEE Transactions on](#)  
Volume 54, Issue 2, Feb. 2007 Page(s):308 - 315  
Digital Object Identifier 10.1109/TED.2006.888752  
[AbstractPlus](#) | Full Text: [PDF](#)(268 KB) IEEE JNL  
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- ☐ 2. **A single-chip decoder for the POCSAG radiopaging code**  
Drude, S.;  
[Consumer Electronics, IEEE Transactions on](#)  
Volume 36, Issue 4; Nov 1990 Page(s):897 - 902  
Digital Object Identifier 10.1109/30.61571  
[AbstractPlus](#) | Full Text: [PDF](#)(360 KB) IEEE JNL  
[Rights and Permissions](#)
- ☐ 3. **Incremental recovery in main memory database systems**  
Levy, E.; Silberschatz, A.;  
[Knowledge and Data Engineering, IEEE Transactions on](#)  
Volume 4, Issue 6, Dec. 1992 Page(s):529 - 540  
Digital Object Identifier 10.1109/69.180604  
[AbstractPlus](#) | Full Text: [PDF](#)(1180 KB) IEEE JNL  
[Rights and Permissions](#)
- ☐ 4. **Bias-dependent collapse and its recovery phenomenon in AlGaAs/GaAs : low temperatures**  
Hori, Y.; Kuzuhara, M.; Samoto, N.; Itoh, T.;  
[Electron Devices, IEEE Transactions on](#)  
Volume 39, Issue 12, Dec. 1992 Page(s):2720 - 2725  
Digital Object Identifier 10.1109/16.168746  
[AbstractPlus](#) | Full Text: [PDF](#)(540 KB) IEEE JNL  
[Rights and Permissions](#)
- ☐ 5. **A vertically integrated bipolar storage cell in 6H silicon carbide for nonv applications**  
Xie, W.; Cooper, J.A., Jr.; Melloch, M.R.; Palmour, J.W.; Carter, C.H., Jr.;  
[Electron Device Letters, IEEE](#)

Volume 15, Issue 6, June 1994 Page(s):212 - 214  
Digital Object Identifier 10.1109/55.286695

[AbstractPlus](#) | [Full Text: PDF\(220 KB\)](#) IEEE JNL  
[Rights and Permissions](#)

- ☐ **6. NV-SRAM: a nonvolatile SRAM with backup ferroelectric capacitors**  
Miwa, T.; Yamada, J.; Koike, H.; Toyoshima, H.; Amanuma, K.; Kobayashi, S.;  
Maejima, Y.; Hada, H.; Kunio, T.;  
[Solid-State Circuits, IEEE Journal of](#)  
Volume 36, Issue 3, March 2001 Page(s):522 - 527  
Digital Object Identifier 10.1109/4.910492  
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(140 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
  
- ☐ **7. Hardware-supported asynchronous checkpointing scheme**  
Chiu, J.-F.; Chiu, G.-M.;  
[Computers and Digital Techniques, IEE Proceedings-](#)  
Volume 145, Issue 2, March 1998 Page(s):109 - 115  
[AbstractPlus](#) | [Full Text: PDF\(876 KB\)](#) IET JNL
  
- ☐ **8. An extended delta compression algorithm and the recovery of failed updr  
embedded systems**  
Terazono, K.; Okada, Y.;  
[Data Compression Conference, 2004. Proceedings. DCC 2004](#)  
23-25 March 2004 Page(s):570  
Digital Object Identifier 10.1109/DCC.2004.1281546  
[AbstractPlus](#) | [Full Text: PDF\(199 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **9. Device state recovery in non-volatile main memory systems**  
Ohmura, R.; Yamasaki, N.; Anzai, Y.;  
[Computer Software and Applications Conference, 2003. COMPSAC 2003. Pro](#)  
[Annual International](#)  
3-6 Nov. 2003 Page(s):16 - 21  
[AbstractPlus](#) | [Full Text: PDF\(290 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **10. A 512 Kbit low-voltage NV-SRAM with the size of a conventional SRAM**  
Miwa, T.; Yamada, J.; Koike, H.; Nakura, T.; Kobayashi, T.; Kasai, N.; Toyoshi  
[VLSI Circuits, 2001. Digest of Technical Papers. 2001 Symposium on](#)  
14-16 June 2001 Page(s):129 - 132  
Digital Object Identifier 10.1109/VLSIC.2001.934217  
[AbstractPlus](#) | [Full Text: PDF\(404 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **11. FeRAM device and circuit technologies fully compatible with advanced C**  
Toyoshima, H.; Kobayashi, S.; Yamada, J.; Miwa, T.; Koike, H.; Takeuchi, H.; I  
N.; Maejima, Y.; Tanabe, N.; Tatsumi, T.; Hada, H.;  
[Custom Integrated Circuits, 2001. IEEE Conference on.](#)  
6-9 May 2001 Page(s):171 - 178  
Digital Object Identifier 10.1109/CICC.2001.929749  
[AbstractPlus](#) | [Full Text: PDF\(800 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **12. NV-SRAM: a nonvolatile SRAM with back-up ferroelectric capacitors**  
Miwa, T.; Yamada, J.; Koike, H.; Toyoshima, H.; Amanuma, K.; Kobayashi, S.;  
Maejima, Y.; Hada, H.; Kunio, T.;  
[Custom Integrated Circuits Conference, 2000. CICC. Proceedings of the IEEE](#)  
21-24 May 2000 Page(s):65 - 68

Digital Object Identifier 10.1109/CICC.2000.852619

[AbstractPlus](#) | Full Text: [PDF](#)(368 KB) IEEE CNF  
[Rights and Permissions](#)

- ☐ **13. Shadowing-based crash recovery schemes for real-time database system**  
LihChyun Shu; Huey-Min Sun; Tei-Wei Kuo;  
[Real-Time Systems, 1999. Proceedings of the 11th Euromicro Conference on](#)  
9-11 June 1999 Page(s):260 - 267  
Digital Object Identifier 10.1109/EMRTS.1999.777473  
[AbstractPlus](#) | Full Text: [PDF](#)(260 KB) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **14. Recovery in a shared disk parallel database system using DFeRAM**  
Yong Keol Kim; Young Sik Baek; Seong Il Jin; Sung Hyun Cho;  
[High Performance Computing on the Information Superhighway, 1997. HPC A](#)  
28 April-2 May 1997 Page(s):627 - 632  
Digital Object Identifier 10.1109/HPC.1997.592221  
[AbstractPlus](#) | Full Text: [PDF](#)(424 KB) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **15. Ferroelectric memory applications**  
Scott, J.F.; Araujo, C.A.; McMillan, L.D.;  
[Ultrasonics Symposium, 1989. Proceedings., IEEE 1989](#)  
3-6 Oct. 1989 Page(s):299 - 308 vol.1  
Digital Object Identifier 10.1109/ULTSYM.1989.66999  
[AbstractPlus](#) | Full Text: [PDF](#)(1332 KB) IEEE CNF  
[Rights and Permissions](#)
  
- ☐ **16. Incremental restart [database management]**  
Levy, E.;  
[Data Engineering, 1991. Proceedings. Seventh International Conference on](#)  
8-12 April 1991 Page(s):640 - 648  
Digital Object Identifier 10.1109/ICDE.1991.131513  
[AbstractPlus](#) | Full Text: [PDF](#)(792 KB) IEEE CNF  
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